

[First Hit](#) [Fwd Refs](#) [Previous Doc](#) [Next Doc](#) [Go to Doc#](#)

☐ [Generate Collection](#) [Print](#)

L1: Entry 9 of 34

File: USPT

Nov 20, 2001

US-PAT-NO: 6321324

DOCUMENT-IDENTIFIER: US 6321324 B1

TITLE: Device for putting an integrated circuit into operation

DATE-ISSUED: November 20, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
<u>Le Van Suu</u> ; Maurice	Romainville			FR

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE	CODE
SGS-Thomson Microelectronics S.A.	Gentilly			FR		03

APPL-NO: 09/ 371773 [\[PALM\]](#)

DATE FILED: August 10, 1999

PARENT-CASE:

CROSS REFERENCE TO RELATED APPLICATIONS This application is a continuation of application Ser. No. 08/636,634, filed Apr. 23, 1996, entitled A CIRCUIT FOR LOADING A MEMORY WITH RULES FOR A FUZZY LOGIC MICROPROCESSOR UPON START-UP OF THE CIRCUIT, which prior application is incorporated herein by reference, now U.S. Pat. No. 6,035,385.

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	APPL-DATE
FR	95 05177	April 28, 1995

INT-CL: [07] G06 F 15/16

US-CL-ISSUED: 712/34; 706/1

US-CL-CURRENT: 712/34; 706/1

FIELD-OF-SEARCH: 712/34, 706/1

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

[Search Selected](#) [Search All](#) [Clear](#)

PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
<input type="checkbox"/> <u>4720812</u>	January 1988	Kao et al.	395/652

<input type="checkbox"/> <u>4837735</u>	June 1989	Allen, Jr. et al.	706/10
<input type="checkbox"/> <u>5361371</u>	November 1994	Kawamoto et al.	395/800
<input type="checkbox"/> <u>5410721</u>	April 1995	Divine et al.	395/595
<input type="checkbox"/> <u>5600757</u>	February 1997	Yamamoto et al.	706/1
<input type="checkbox"/> <u>5706478</u>	January 1998	Dye	395/503
<input type="checkbox"/> <u>6035385</u>	March 2000	Le Van Suu	712/34

FOREIGN PATENT DOCUMENTS

FOREIGN-PAT-NO	PUBN-DATE	COUNTRY	US-CL
A-42 25 758	February 1996	DE	
A-0 364 743	April 1990	EP	
A-0 455 345	November 1991	EP	

OTHER PUBLICATIONS

French Search Report from French Patent Application No. 95 05177, filed Apr. 28, 1995.

Electronic Engineering, vol. 65, No. 795, Mar. 1, 1993, p. 83 "Combining Fuzzy Hard-Ware With the Arm Core".

Kourra et al., "Dedicated Silicon Solutions for Fuzzy Logic Systems," IEE London, UK pp 3/1-3/12, 5/93.

Ikeda et al., "A Fuzzy Inference Coprocessor Using a Flexible Active-Rule-Driven Architecture," IEEE pp 537-544, Mar. 8, 1992.

ART-UNIT: 214

PRIMARY-EXAMINER: Follansbee; John A.

ATTY-AGENT-FIRM: Wolf, Greenfield & Sacks, P.C. Morris; James H.

ABSTRACT:

To enable the putting into use of a monolithic integrated circuit comprising a processor and a fuzzy logic coprocessor, both having a single program memory in common, an operation is effected by which, at the time of the initializing of the integrated circuit, a volatile, random-access memory of the coprocessor is loaded with instructions stored in this single program memory.

28 Claims, 3 Drawing figures

[Previous Doc](#) [Next Doc](#) [Go to Doc#](#)